ABSTRACT OF THE DISCLOSURE

[0051] A sigma delta converter configured for DC offset correction is presented. The sigma delta modulator has integrator circuitry including an integrator input and an integrator output. An input signal received at the integrator input has an input AC voltage component and a DC offset component. Capacitors are connected to the integrator input, and a first set of switches is connected to the pair of capacitors. The first set of switches transfer a first charge to the pair of capacitors during a first phase, and a second set of switches transfer the first charge and a second charge to the integrator input.